

In the Claims:

Please amend claims 1-15 as indicated below. This listing of claims replaces all prior versions.

1. (*Cancelled*)

2. (*Currently Amended*) ~~A method as claimed in claim 1;~~ A method for writing a data value to a location within non-volatile (NV) memory of a smart card, the smart card having a processor that is adapted to read from and write to the NV memory, the method comprising:
_____ providing an instruction to the processor of the smart card, the instruction containing the data value and an address pointer that identifies the location within the NV memory; and
_____ using the processor, writing the data value to the location within the NV memory identified by the address pointer; and
_____ characterized in that, for writing to the NV memory, an instruction set of the processor is extended by additional move code write instructions (MOVCWR instructions).

3. (*Previously Presented*) A method as claimed in claim 2, characterized in that the additional instructions of the processor perform a transfer of parameters for address pointer and for the data value to be written and activate corresponding control signals for a memory management unit (MMU) and NV memory interfaces.

4. (*Previously Presented*) A method as claimed in claim 2, characterized in that ~~the~~ address processing for the MOVCWR instructions is performed in the same way as the processing of code fetches or MOVC instructions, in the presence of a memory management unit (MMU).

5. (*Previously Presented*) A method as claimed in claim 3, characterized in that the MMU is extended by a control signal path in the presence of a memory management unit (MMU) of the processor.

Claims 6-9 (*Cancelled*)

10. (*Currently Amended*) ~~A method as claimed in claim 9;~~ A method for writing a data value to a location within non-volatile (NV) memory of a smart card, the smart card having a processor that is adapted to read from and write to the NV memory, the method comprising:

_____ providing an instruction to the processor of the smart card, the instruction containing the data value and an address pointer that identifies the location within the NV memory; and

_____ using the processor, writing the data value to the location within the NV memory identified by the address pointer;

_____ characterized in that the content of a cache page register is programmed into the NV memory by writing to a control register of the NV memory; and

_____ characterized in that the cache page register of the NV memory is cleared when changing to a new page address in ~~the~~ an event of an MOVCWR instruction.

11. (*Currently Amended*) ~~A method as claimed in claim 1;~~ A method for writing a data value to a location within non-volatile (NV) memory of a smart card, the smart card having a processor that is adapted to read from and write to the NV memory, the method comprising:

_____ providing an instruction to the processor of the smart card, the instruction containing the data value and an address pointer that identifies the location within the NV memory; and

_____ using the processor, writing the data value to the location within the NV memory identified by the address pointer; and

_____ characterized in that undesired programming of old page register contents under incorrect addresses is prevented.

Claims 12-15 (*Cancelled*)